

## ABSTRACT OF THE DISCLOSURE

- An outer conductor, an input terminal electrode, an output terminal electrode, a voltage controllable terminal electrode, and two separated electrodes are formed on the outer face of a dielectric block.

  Furthermore, on the upper face of the dielectric
- block, PIN diodes which are voltage controllable reactance elements, and inductors for voltage-controlling the PIN diodes, and a coupling adjustment capacitor are mounted. Frequency shifting capacitors are formed by generation of electrostatic capacitances
- between the separated electrodes and the inner conductors of the resonance holes, respectively.